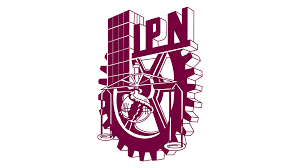
INSTITUTO POLITECNICO NACIONAL

ESCUELA SUPERIOR DE COMPUTO

Práctica 4

“Piano con Mux y Decodificador”

Diseño de Sistemas Digitales

Rene Baltazar Jiménez Ruiz

Equipo 7

Hernández Hernández Roberto Issac

Navarrete Piña Arath Antonio

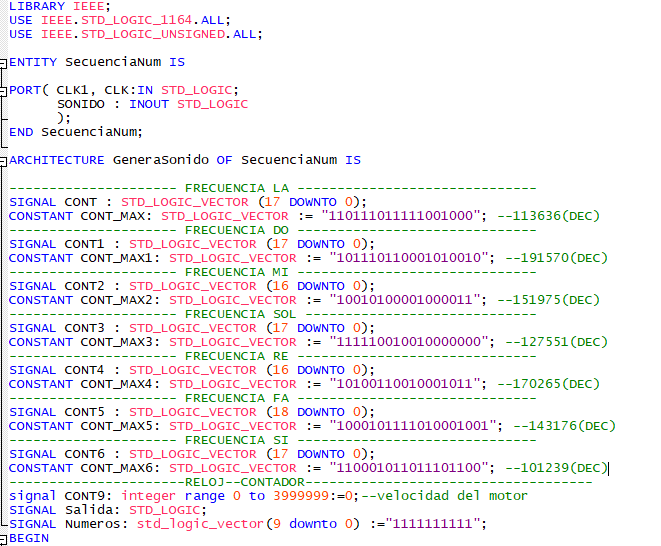
Yescas García Ulysses Aarón

4CV2

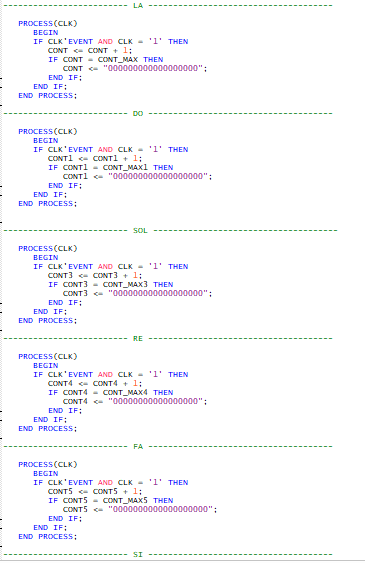
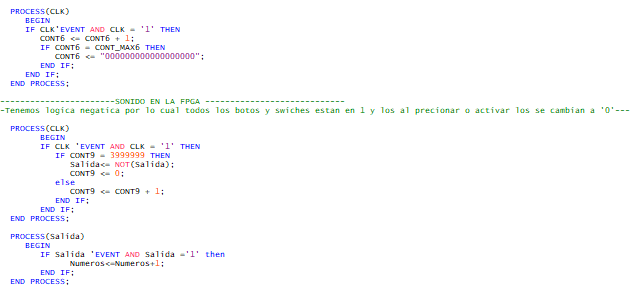
DIAGRAMA

CODIGO VHDL

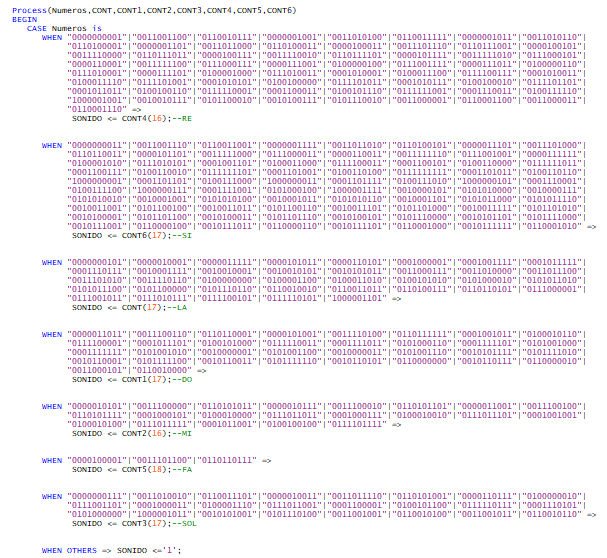






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